# ROHA

# **LEADER TIME SRL**

# **PRODUCT SPECIFICATION**

# 16\*2 Characters COB LCD MODULE MODEL: LT-1602I-201 Ver:1.0

< <> > Finally Specification

CUSTOMER'S APPROVAL					
CUSTOMER:					
SIG	NATURE:	DATE:			

APPROVED	РМ	PD	PREPARED
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# Prepared By: LEADER TIME SRL

VIA MONS. PROSDOCIMI, 27 36042 BREGANZE (VI)

• This specification is subject to change without notice. Please contact LT or it's representative before designing your product based on this specification.

# **Revision Status**

Version	Revise Date	Page	Content	Modified By
Ver 1.0	2014.09.20		First Issued	

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# 1. Features

The features of LCD are showed as follows

\* Display mode : STN/ Yellow-Green/Transflective/Positive \* Controller IC : SPLC780D1-001(English-Japanese)

\* Display format : 16X2 Characters \* Interface Input Data : 4 bit or 8 bit MPU \* Driving Method : 1/16Duty, 1/5Bias

\* Viewing Direction : 6 O'clock

\* Backlight :LED,Bottom Yellow-Green

\*Sample NO. :

# 2. MECHANICAL SPECIFICATIONS

Item	Specification	Unit
Module Size	60(W) x 36(H) x 12.5MAX (D)	mm
Viewing Area	52(W) x 15(H)	mm
Activity Display Area	47.45(W) x 8.9(H)	mm
Character Font	5x8 Dots	-
Character Size	2.45(W)x3.95(H)	mm
Character Pitch	3(W)x4.95(H)	mm
Dot Size	0.45(W)x0.45(H)	mm

# 3. ELECTRICAL SPECIFICATIONS

# 3-1 ABSOLUTE MAXIMUM RATINGS (Ta = 25 °C)

Item	Symbol	Min	Max	Unit
Supply Voltage For Logic	Vdd	-0.3	+7.0	V
Supply Voltage For LCD Drive	$V_{LCD}$	VDD-10.0	VDD+0.3	٧
Input Voltage	Vin	-0.3	VDD+0.3	٧
Operating Temp.	Тор	-20	+70	°C
Storage Temp.	Tst	-30	+80	°C

<sup>\*.</sup> NOTE: The response time will be extremely slow when the operating temperature is around -10°C, and the back ground will become darker at high temperature operating.

# **3-2 ELECTRICAL CHARACTERISTICS**

It	em	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Logic supply Voltage		$V_{\scriptscriptstyle DD}$ – Vss		-	5	-	V
LCD Drive		$V_{OP} = V_{DD} - V5$		4.3	4.5	4.7	V
	"H" Level (Except OSC1)	V IH1		0.7 <i>V</i> <sub>DD</sub>	-	$V_{\scriptscriptstyle DD}$	V
	"L" Level (Except OSC1)	V <sub>IL1</sub>	Ta = 25 °C VDD=5V±5%	-0.3	-	0.55	V
Input Voltage	"H" Level (OSC1)	V <sub>IH2</sub>	VBB 0V = 070	$0.7V_{DD}$	-	$V_{\scriptscriptstyle DD}$	V
	"L" Level (OSC1)	V <sub>IL2</sub>		-0.2	-	$0.2V_{DD}$	٧
Frame F	requency	f <sub>FLM</sub>		-	75	-	Hz
Current C	onsumption	I <sub>DD</sub>		-	T.B.D.	-	mA

### 3-3 BACKLIGHT

# 3-3-1. Absolute Maximum Ratings

Item	Symbol	Condition	min	Тур	Max	Unit
Forward Current	IF	Ta = 25 °C	-	-	17*60	mA
Power Dissipation	PD	1a - 25 C	-	-	33*50	mW
Reverse Current	IR	VR=10V/LED	-	-	170	uA

## 3-3-2. Electrical-optical Characteristics

Item	Symbol	Condition	min	Тур	Max	Unit
Forward Voltage	VF		4.0	4.2	4.4	V
Average Luminous Intensity	lv	If=170mA Ta = 25 °C	1	220	-	cd/m <sup>2</sup>
Peak wave length	λР		569	-	576	nm

The brightness is measured without LCD panel

For operation above 25 °C,The lfm & Pd must be derated , the current derating is -0.36mA/ °C for DC drive and -0.86mA/ °C for Pulse drive ,the Power dissipation is -0.75mW/ °C.The product working current must not more than the 60% of the lfm or lfp according to the working temperature.

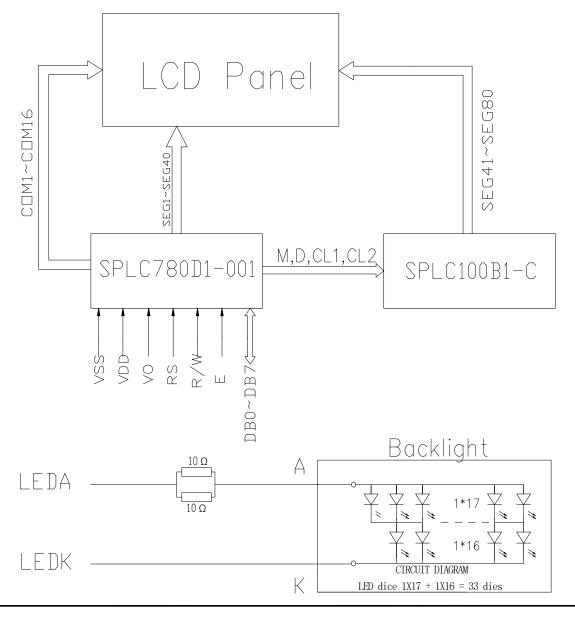
Backlight lifetime means luminance value larger than half of the original after 20000 hours' continuous working.

# 4. TERMINAL FUNCTIONS AND BLOCK DIAGRAM

# **4-1 INTERFACE PIN FUNCTION DESCRIPTION**

PIN NO.	SYMBOL	FUNCTIONS			
1	VSS	Ground			
2	VDD	Supply voltage for logical circuit			
3	V0	Supply voltage for LCD driving			
4	RS	A signal for selecting registers.  1: Data Register (for read and write)  0: Instruction Register (for write)			
5	R/W	A signal for selecting read or write actions.1: Read, 0: Write.			
6	E	A enable signal for reading or writing data.			
7-14	DB0~DB7	8 Bit Data Bus			
15	LEDA	Backlight(+5V)			
16	LEDK	Backlight(-)			

# **4-2 BLOCK DIAGRAM**



# **5. TIMING CHARACTERISTICS**

Data Output Delay Time

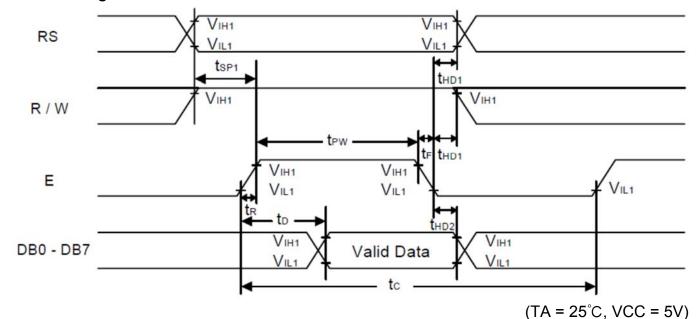
Data hold time

 $t_D$ 

t<sub>HD2</sub>

5.0

# 5-1 Reading Data from SPLC780D1 to MPU



T/o	20					· · · · · · · · · · · · · · · · · · ·
Oh amandamindi an	0		Limit			Total Constitution
Characteristics	Symbol	Min.	Тур.	Max.	Unit	Test Condition
E Cycle Time	tc	400	-	-	ns	Pin E
E Pulse Width	t <sub>W</sub>	150	-	-	ns	Pin E
E Rise/Fall Time	t <sub>R</sub> , t <sub>F</sub>	-	_	25	ns	Pin E
Address Setup Time	t <sub>SP1</sub>	30		-	ns	Pins: RS, R/W, E
Address Hold Time	t <sub>HD1</sub>	10	_	-	ns	Pins: RS, R/W, E

100

ns

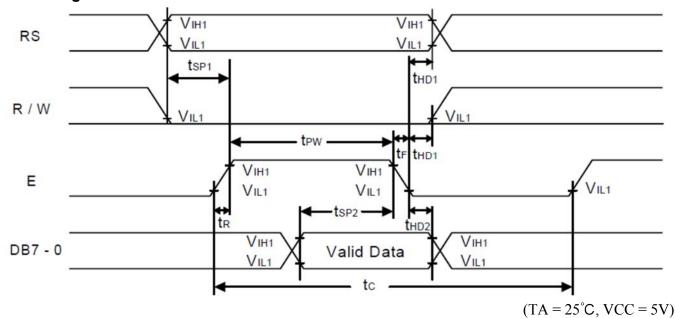
ns

Pins: DB0 - DB7

Pin DB0 - DB7

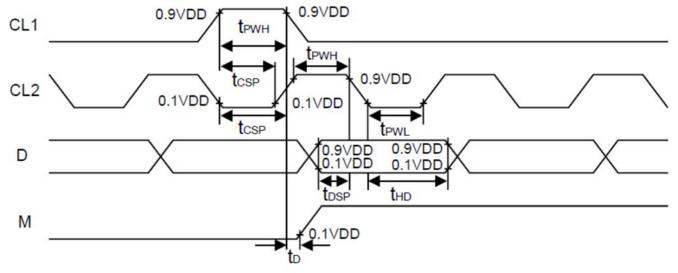


# 5-2 Writing Data from MPU to SPLC780D1



Characteristics	0	Limit				
Characteristics	Characteristics Symbol Min. Typ. Max.	Unit	Test Condition			
E Cycle Time	t <sub>C</sub>	400	87 <del>.</del>	-	ns	Pin E
E Pulse Width	t <sub>PW</sub>	150	-	-	ns	Pin E
E Rise/Fall Time	t <sub>R</sub> , t <sub>F</sub>	-	-	25	ns	Pin E
Address Setup Time	t <sub>SP1</sub>	30	1/2		ns	Pins: RS, R/W, E
Address Hold Time	t <sub>HD1</sub>	10	-	-	ns	Pins: RS, R/W, E
Data Setup Time	t <sub>SP2</sub>	40	:-	-	ns	Pins: DB0 - DB7
Data Hold Time	t <sub>HD2</sub>	10	-	-	ns	Pins: DB0 - DB7

# 5-3 Interface mode with SPLC100B1 timing diagram



 $(TA = 25^{\circ}C, VCC = 5V)$ 

Oh ann at a tatta			Limit			Total Constitution	
Characteristics	Symbol	Min.	Тур.	Max.	Unit	Test Condition	
Clock pulse width high	t <sub>PWH</sub>	800	-	-	ns	Pins: CL1, CL2	
Clock pulse width low	t <sub>PWL</sub>	800		-	ns	Pins: CL1, CL2	
Clock setup time	t <sub>CSP</sub>	500		-	ns	Pins: CL1, CL2	
Data setup time	t <sub>DSP</sub>	300	•	•	ns	Pins: D	
Data hold time	t <sub>HD</sub>	300		•	ns	Pins: D	
M delay time	t <sub>D</sub>	-1000	-	1000	ns	Pins: M	

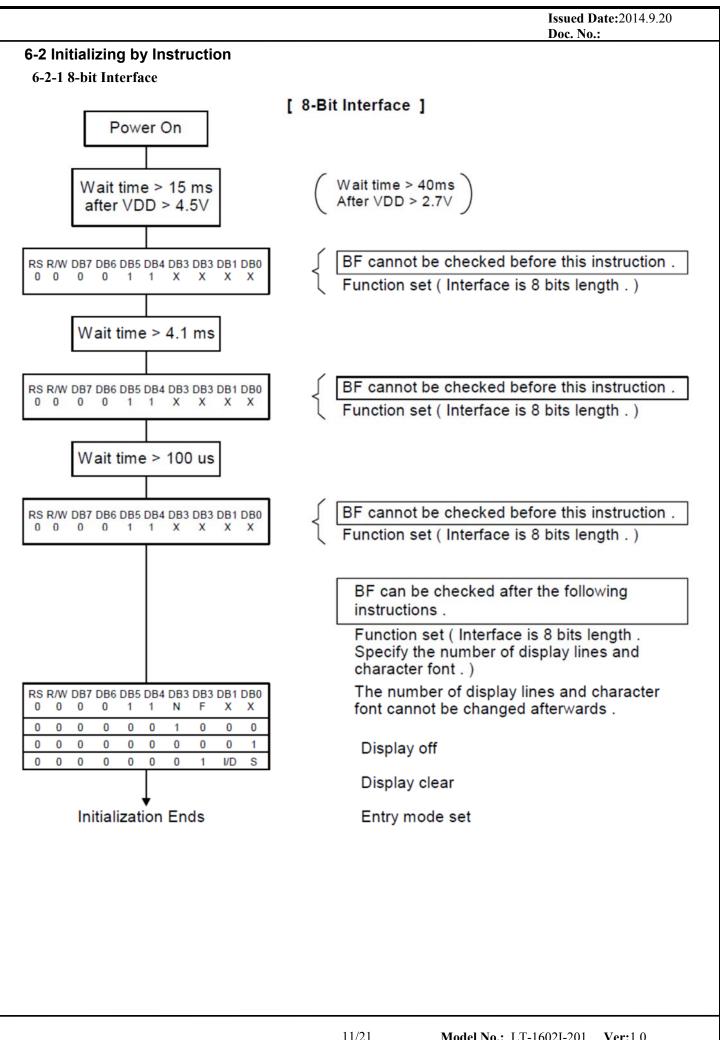
# **6 COMMAND LIST**

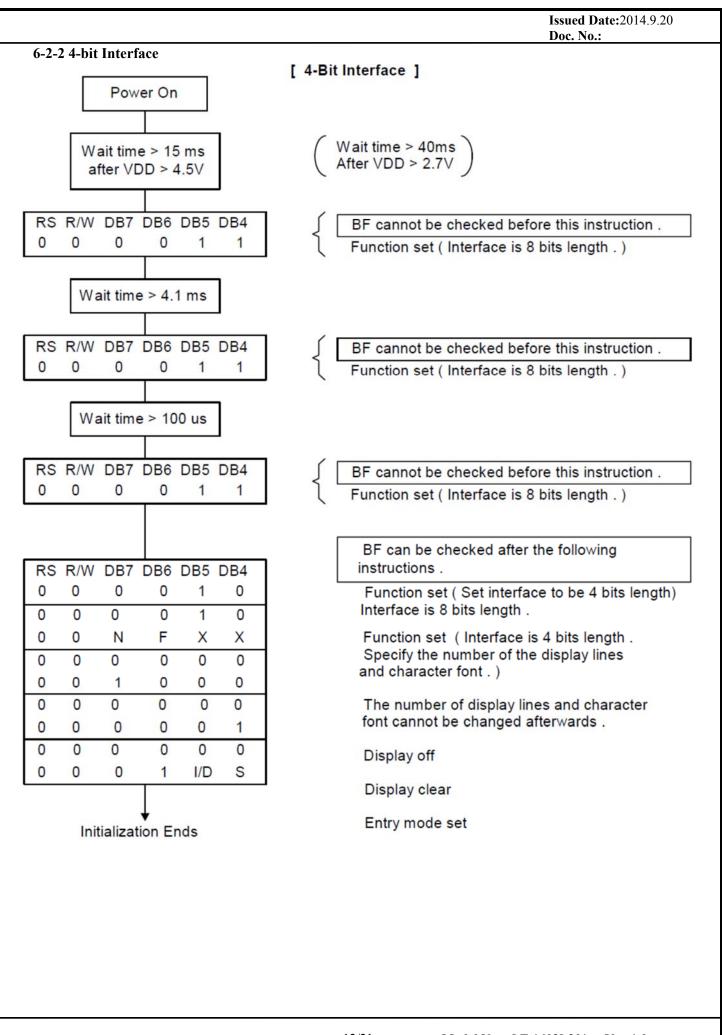
# **6-1 Instruction Table**

In admiration		Instruction Code									D		ecution tir	
Instruction	RS	RW	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	Description	Fosc=	Fosc= 270KHz	Fosc=
Clear Display	0	0	0	0	0	0	0	0	0	1	Write "20H" to DDRAM and set DDRAM address to "00H" from AC	2.16ms	1.52ms	1.18ms
Return Home	0	0	0	0	0	0	0	0	1	ï	Set DDRAM address to "00H" from AC and return cursor to its original position if shifted. The contents of DDRAM are not changed.	2.16ms	1.52ms	1.18ms
Entry Mode Set	0	0	0	0	0	0	0	1	I/D	Ø	Assign cursor moving direction and enable the shift of entire display	53μs	38μs	29μs
Display ON/ OFF Control	0	0	0	0	0	0	1	۵	O	ß	Set display (D), cursor(C), and blinking of cursor(B) on/off control bit.	53μ <b>s</b>	38μ <b>s</b>	29μ <b>s</b>
Cursor or Display Shift	0	0	0	0	0	1	S/C	R/L	-	,	Set cursor moving and display shift control bit, and the direction, without changing of DDRAM data.	53μ <b>s</b>	38μs	29μ <b>s</b>
Function Set	0	0	0	0	1	DL	N	F	-	-	Set interface data length (DL: 8-bit/4-bit), numbers of display line (N: 2-line/1-line) and, display font type (F:5x10 dots/5x8 dots)	53μ <b>s</b>	38μ <b>s</b>	29μs
Set CGRAM Address	0	0	0	1	AC5	AC4	AC3	AC2	AC1	AC0	Set CGRAM address in address counter.	53μs	38μs	29μs
Set DDRAM Address	0	0	1	AC6	AC5	AC4	AC3	AC2	AC1	AC0	Set DDRAM address in address counter	53μs	38µs	29µs
Read Busy Flag and Address Counter	0	1	BF	AC6	AC5	AC4	AC3	AC2	AC1	AC0	Whether during internal operation or not can be known by reading BF. The contents of address counter can also be read.			
Write Data to RAM	1	0	D7	D6	D5	D4	D3	D2	D1	D0	Write data into internal RAM (DDRAM/CGRAM).	53μs	38μs	29μ <b>s</b>
Read Data from RAM	1	1	D7	D6	D5	D4	D3	D2	D1	D0	Read data from internal RAM (DDRAM/CGRAM).	53μ <b>s</b>	38μs	29μ <b>s</b>

Note1: "--": don't care

Note2: In the operation condition under -20°C ~ 75°C, the maximum execution time for majority of instruction sets is 100us, except two instructions, "Clear Display" and "Return Home", in which maximum execution time can take up to 4.1ms.



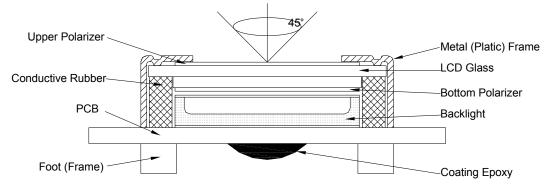


# 7.CHARACTER GENERATOR ROM

Upper															
4 bit Lower 4 bit	LLLL	LLLH	LLHL	LLHH	LHLL	LHLH	LHHL	HLLL	HLLH	HLHL	HLHH	HHLL	HHLH	HHHL	нннн
LLLL															
LLLH															
LLHL															
ггнн															
LHLL															
LHLH															
LHHL															
гннн															
HLLL															
HLLH															
нгнг															
нгнн															
HHLL															
ннгн															
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# 8. QUALITY SPECIFICATIONS

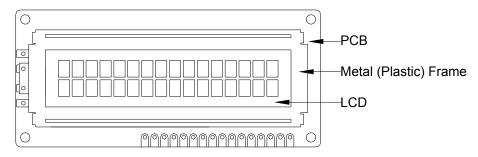
- 8-1. LCM Appearance and Electric inspection Condition
  - 1. Inspection will be done by placing LCM 30cm away from inspector's eyeballs under normal illumination.



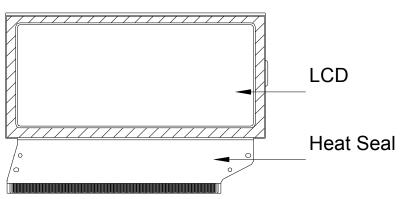
2. View Angle: with in 45° around perpendicular line.

### 8-2. Definition

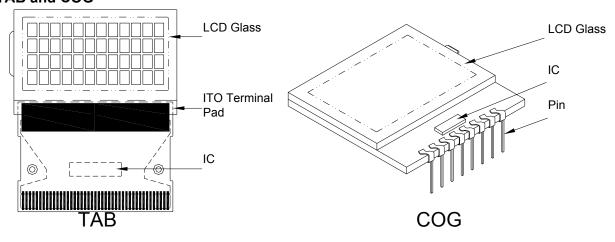
1. COB



2. Heat Seal



3. TAB and COG



# 8-3. Sampling Plan and Acceptance

1.Sampling Plan

MIL - STD - 105E (  $\parallel$  ) ordinary single inspection is used.

2.Acceptance

Major defect: AQL = 0.65% Minor defect: AQL = 1.5%

# 8-4. Criteria

# 1.COB

Defect	Inspection Item	Inspection Standards					
Major	PCB copper flakes peeling off	Any copper flake in viewing Area should be greater than 1.0mm <sup>2</sup>	Reject				
Major	Height of coating epoxy	Exceed the dimension of drawing	Reject				
Major	Void or hole of coating epoxy	Expose bonding wire or IC	Reject				
Major	PCB cutting defect	Exceed the dimension of drawing	Reject				

## 2.SMT

Defect	Inspection Item	Inspection Standa	ards
Minor	Component marking not readable		Reject
Minor	Component height	Exceed the dimension Of drawing	Reject
Major	Component solder defect (missing, extra, wrong component or wrong orientation		Reject
Minor	Component position shift  component soldering pad  X  D  Y	X < 3/4Z Y > 1/3D	Reject Reject
Minor	Component tilt component soldering pad	Y > 1/3D	Reject
Minor	Insufficient solder component PAD	<i>θ</i> ≤ 20°	Reject

# 3. Metal (Plastic) Frame

Defect	Inspection Item	In	spection Standa	rds			
Major	Crack / breakage	Any	where	Reject			
		W	L	Acceptable of Scratch			
		w<0.1mm	Any	Ignore			
Minor		0.1 <u>&lt;</u> w<0.2mm	L <u>&lt;</u> 5.0mm	2			
	Frame Scratch	0.2 <u>&lt;</u> w<0.3mm	L <u>&lt;</u> 3.0mm	1			
		w <u>&gt;</u> 0.3mm	Any	0			
		with distance gro 2. Scratch or can be ignored.	frame (not visible				
				Acceptable of Dents / Pricks			
		Φ <u>&lt;</u> 1	2				
	Frame Dent , Prick	1.0<⊕	1				
Minor	$\Phi = \frac{L + W}{2}$	1.5m	1.5mm<⊕				
	2	Note: 1. Above criteria applicable to any two dents / pricks with distance greater than 5mm 2. Dent / prick on the back side of frame (no visible) can be ignored					
Minor	Frame Deformation	Exceed	d the dimension of	drawing			
Minor	Metal Frame Oxidation		Any rust				

# 4. Flexible Film Connector (FFC)

Defect	Insp	ection Item	Inspection Standards					
Minor	Tilted soldering		Within the angle +5°	Acceptable				
Minor	Uneven solder joint /bump			Reject				
			Expose the conductive line	Reject				
Minor	Hole	$\Phi = \frac{\Gamma + M}{2}$	Ф > 1.0mm	Reject				
Minor	Position shift  Y  Alice  Position shift  Position shift	Y > 1/3D	Reject					
WIIIIOI	- A	- <del>-</del>	X > 1/2Z	Reject				

# 5. Screw

Defect	Inspection Item	Inspection Standards					
Major	Screw missing/loosen		Reject				
Minor	Screw oxidation	Any rust	Reject				
Minor	Screw deformation	Difficult to accept screw driver	Reject				

# 6. Heatseal 、TCP 、FPC

Defect	Inspection Item	Inspection Standards	
Major	Scratch expose conductive layer		Reject
Minor	HS Hole $\Phi = \frac{L + W}{2}$	Ф> 0.5mm	Reject
Major	Adhesion strength	Less than the specification	Reject
Minor	Position shift	Y > 1/3D	Reject
IVIIIIOI	-**-	X > 1/2Z	Reject
Major	Conductive line break		Reject

7. LED Backing Protective Film and Others

Defect	Inspection Item	Inspection Standards			
		Acceptable number of units			
		⊕ <u>&lt;</u> 0.10mm	Ignore		
		0.10<⊕ <u>&lt;</u> 0.15mm	2		
Minor	LED dirty, prick	0.15<⊕ <u>&lt;</u> 0.2mm	1		
		Ф>0.2mm	0		
		The distance between any two spots should be ≥5mm Any spot/dot/void outside of viewing area is acceptable			
Minor	Protective film tilt	Not fully cover LCD	Reject		
Major	COG coating	Not fully cover ITO circuit	Reject		

8. Electric Inspection

<u> </u>	. Elocato mopocación									
Defect	Inspection Item	Inspection Standards								
Major	Short		Reject							
Major	Open		Reject							

# 9. Inspection Specification of LCD

Defect	Insp	ect Item	Inspection Standards							
		* Glass Scratch	W		W <u>&lt;</u>	0.03	0.0	0.0 <u><!--</u--></u>	5 V	V>0.05
		* Polarizer Scratch	L		L٠	<5		L<3		Any
Minor	Linear Defect	* Fiber and Linear	ACC. NO.			1		1		Reject
		material	Note	L is the	e ler	ngth and V	/ is th	is the width of the de		
		* Foreign material		Φ <u>&lt;</u> 0		0.1<⊕ <u>&lt;</u> 0	.15 (	15 0.15<⊕ <u>&lt;</u> 0.2		⊕>0.2
Minor Black Spot and Polarizer Pricked	between glass and polarizer or glass and		3EA 100m		2		1		0	
		glass  * Polarizer hole or protuberance by external force	Note		$\Phi$ is the average diameter of the defect. Distance between two defects > 10mm.					
		* Unobvious	_		Φ <u>&lt;</u> (	0.3	0.3	<⊕ <u>&lt;</u> 0.5	0.	5<⊕
White Sp	White Spot	transparant foreign material between		3EA	./ 10	00mm²		1		0
Minor and Bubble in polarizer		glass and glass or glass and polarizer * Air protuberance between polarizer and glass		1		•		of the defects > 10n		
			Φ	Φ <u>&lt;</u> 0.		0.10<⊕≤	0.20	0.20<⊕ <u>&lt;</u> 0.25		Φ>0.25
		<u>w</u>	ACC. NO.	3EA 100m		2		1		0
Minor	Segment Defect			W is m	ore	than 1/2 s	egme	nt width		Reject
			Note	$\Phi = \frac{L + W}{2}$ Distance between two defect is 10mm						
			Φ	Φ <b>&lt;</b> 0.	10	0.10<⊕≤	≤0.20 0.20<⊕≤0.25		0.25	Ф>0.25
	Protuberant	w W	W	Glue	е	W <u>&lt;</u> 1/2 \$ W <u>&lt;</u> 0.		W <u>&lt;</u> 1/2 ∶ W <u>&lt;</u> 0.		Ignore
Minor	Segment	$\Phi = (L + W)/2$	ACC. NO.	3EA 100m		2		1		0
			1. Seg	ment						•
			Е	-		0.4mm		3 <u>&lt;</u> 1.0mm		I.0mm
Minor	Assembly		B-			A<1/2B		A<0.2		<0.25
14111101	Mis-alignment	HB-1 -1 HA	Judge Acceptable Acceptable Acceptable						eptable	
			2. Dot Matrix  Deformation>2° Reje						Reject	
Minor	Stain on LCD Panel Surface		Accept when stains can be wiped lightly with a soft cloth of a similar one. Otherwise, judged according to the abovitems: "Black spot" and "White Spot"							

18/21 **Model No.:** C1602C4SBY6B-B0 **Ver:**1.0

# 9. RELIABILITY

No	Item	Condition	Quantity	Criteria
1	High Temperature Operating	70°C, 96Hrs	2	GB/T2423.2 -2008
2	Low Temperature Operating	-20°C, 96Hrs	2	GB/T2423.1 -2008
3	High Humidity	50°C, 90%RH, 96Hrs	2	GB/T2423.3 -2006
4	High Temperature Storage	80°C, 96Hrs	2	GB/T2423.2 -2008
5	Low Temperature Storage	-30°C, 96Hrs	2	GB/T2423.1 -2008
6	Thermal Cycling Test	-20°C, 60min~70°C, 60min, 20 cycles.	2	GB/T2423.2 2 -2012
7	Packing vibration	Frequency range:10Hz~50Hz Acceleration of gravity:5G X,Y,Z 30 min for each direction.	2	GB/T5170.1 4 -2009
8	Electrical Static Discharge	Air: $\pm 8$ KV 150pF/330 $\Omega$ 5 times	2	GB/T17626.
		Contact: $\pm 4$ KV 150pF/330 $\Omega$ 5 times		-2006
9	Drop Test (Packaged)	Height: 80 cm, 1 corner, 3 edges, 6 surfaces.	2	GB/T2423.8 -1995

Note: 1) Above conditions are suitable for our company standard products.

2) For restrict products, the test conditions listed as above must be revised.

## 10. HANDLING PRECAUTION

(1) Mounting Method

The panel of the LCD Module consists of two thin glass plates with polarizers which easily get damaged since the Module is fixed by utilizing fitting holes in the printed circuit board. Extreme care should be taken when handling the LCD Modules.

(2) Caution of LCD handling & cleaning

When cleaning the display surface, use soft cloth with solvent (recommended below) and wipe lightly.

- Isopropyl alcohol
- Ethyl alcohol
- Trichloro trifloro thane

Do not wipe the display surface with dry or hard materials that will damage the polarizer surface.

Do not use the following solvent:

- Water
- Ketone
- Aromatics

# (3) Caution against static charge

The LCD Module use C-MOS LSI drivers, so we recommend that you connect any unused input terminal to VDD or VSS, do not input any signals before power is turned on. And ground your body, Work/assembly table. And assembly equipment to protect against static electricity.

### (4) Packaging

- Modules use LCD elements, and must be treated as such. Avoid intense shock and falls from a height.
- To prevent modules from degradation. Do not operate or store them exposed directly to sunshine or high temperature/humidity.

### (5) Caution for operation

- It is indispensable to drive LCD's within the specified voltage limit since the higher voltage than the limit shorten LCD life. An electrochemical reaction due to direct current causes LCD deterioration, Avoid the use of direct current drive.
- Response time will be extremely delayed at lower temperature than the operating temperature range and on the other hand at higher temperature LCD's show dark color in them. However those phenomena do not mean malfunction or out of order with LCD's. Which will come back in the specified operating temperature range.
- If the display area is pushed hard during operation, some font will be abnormally displayed but it resumes normal condition after turning off once.
- A slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit.

Usage under the relative condition of 40°C, 50%RH or less is required.

### (6) Storage

In the case of storing for a long period of time (for instance.) For years) for the purpose or replacement use, The following ways are recommended.

- Storage in a polyethylene bag with sealed so as not to enter fresh air outside in it, And with no desiccant.
- Placing in a dark place where neither exposure to direct sunlight nor light is. Keeping temperature in the specified storage temperature range.
- Storing with no touch on polarizer surface by the anything else. (It is recommended to store them as they have been contained in the inner container at the time of delivery)

### (7) Safety

- It is recommendable to crash damaged or unnecessary LCD into pieces and wash off liquid crystal by using solvents such as acetone and ethanol. Which should be burned up later.

When any liquid crystal leaked out of a damaged glass cell comes in contact with your hands, please wash it off well with soap and water.

### (8) Other

- After the product shipped, any product quality issues must be feedback within three months, otherwise, we will not be responsible for the subsequent or consequential events.

